

**INFORMATION DISCLOSURE
CITATION IN AN
APPLICATION**

(PTO-1449)

ATTY. DOCKET NO.
43876-162SERIAL NO.
**Continuation of Serial
No. 10/646,787**APPLICANT
HANSEN, et al.FILING DATE
January 16, 2004GROUP
To be assigned**U.S. PATENT DOCUMENTS**

EXAMINER'S INITIALS	PATENT NO.	DATE	NAME	CLASS	SUBCLASS	FILING DATE
E.C.	4,025,772	05/24/77	Constant	—	—	
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	4,727,505	02/23/88	Konishi, et al.	—	—	
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	5,032,865	07/16/91	Schlunt	—	—	
	5,157,388	10/20/92	Kohn	—	—	
	5,201,056	04/06/93	Daniel, et al.	—	—	
	5,268,855	12/07/93	Mason, et al.	—	—	
E.C.	5,268,995	12/07/93	Diefendorff, et al.	—	—	

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E.C.	Parallel Computers for Graphics Applications, Adam Levinthal, Pat Hanrahan, Mike Paquette, Jim Lawson, Pixar San Rafael, California, 1987
E.C.	Organization of the Motorola 88110 Superscalar RISC Microprocessor, Keith Diefendorff and Michael Allen, IEEE Micro. April 1992, 40-63
E.C.	Microprocessor Report, Volume 7 Number 13, October 4, 1993, IBM Regains Performance Lead with Power2, Six Way Superscalar CPU in MCM Achieves 126 SPECint92.
E.C.	IBM Creates PowerPC Processors for AS/400, Two New CPU's Implement 64-Bit Power PC with Extensions by Linley Gwennap, Microprocessor Report July 31, 1995, 15-16

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E.C.	5,423,051	06/06/95	Fuller, et al.	—	—	
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E.C.	5,500,811	03/19/96	Corry	—	—	
E.C.	5,557,724	09/17/96	Sampat, et al.	—	—	
E.C.	5,588,152	12/24/96	Dapp, et al.	—	—	
E.C.	5,592,405	01/07/97	Gove, et al.	—	—	
E.C.	5,640,543	06/17/97	Farrell, et al.	—	—	
E.C.	5,642,306	06/24/97	Mennemeier, et al.	—	—	
E.C.	5,666,298	09/09/97	Peleg, et al.	—	—	
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						Yes	No
E.C.	0 474 246 A2	06/09/91	EP	—	—		
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E.C.	The Visual Instruction Set (VIS) in UltraSPARTM, L. Kohn, G. Maturana, M. Tremblay, A. Prabhu, G. Zyner, IEEE, May 3, 1995, 462-469
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E.C.	A General-Purpose Array Processor for Seismic Processing, Nov-Dec., 1984, Volume 1, No. 3) Revisiting past digital signal processor technology, Don Shaver- Jan-Mar. 1998, 5-26
E.C.	Ruby B. Lee, "Accelerating Multimedia with Enhanced Microprocessors", IEEE Micro, April 1995, 22-32.

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E.C.	5,721,892	02/24/98	Peleg, et al.	—	—		
	5,734,874	03/31/98	Van Hook, et al.	—	—		
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	5,825,677	10/20/98	Agarwal, et al.	—	—		
	5,835,782	11/10/98	Chu Lin, et al.	—	—		
	5,886,732	03/23/99	Humbleman	—	—		
	5,922,066	07/13/99	Cho, et al.	—	—		
	5,983,257	11/09/99	Dulong, et al.	—	—		
	6,016,538	01/18/00	Guttag, et al.	—	—		
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E.C.	6,401,194 B1	06/04/02	Nguyen, et al.	—	—		
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U.S. PATENT DOCUMENTS						
EXAMINER'S INITIALS	CITE NO.	Document Number Number-Kind Code(s if known)	Publication Date MM-DD-YYYY	Name of Patentee or Applicant of Cited Document	Pages, Columns, Lines, Where Relevant Passages or Relevant Figures Appear	
<i>E.C.</i>	US	4,785,393	11/15/1988	Chu et al.		
	US	4,814,976	03/21/1989	Craig C. Hansen, et al.		
	US	5,031,135	07/09/1991	Patel		
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	US	6,173,393 B1	01/09/2001	Salvador Palanca, et al.		
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					<input type="checkbox"/> Yes	<input type="checkbox"/> No
OTHER ART (Including Author, Title, Date, Pertinent Pages, Etc.)						
EXAMINER'S INITIALS	CITE NO.	Include name of the author (in CAPITAL LETTERS), title of the article (when appropriate), title of the item (book, magazine, journal, serial, symposium, catalog, etc.), date, page(s), volume-issue number(s), publisher, city and/or country where published.				
<i>E.C.</i>		IEEE Draft Standard for "Scalable Coherent Interface-Low-Voltage Differential Signal Specifications And Packet Encoding", IEEE Standards Department, P1596.3/D0.15 (March 1992)				
<i>E.C.</i>		IEEE Draft Standard for "High-Bandwidth Memory Interface Based on SCI Signaling Technology (RamLink)", IEEE Standards Department, Draft 1.25 IEEE P1596.4-199X (May 1995)				
<i>E.C.</i>		IBM, "The PowerPC Architecture: A Specification For A New Family of Risc Processors", 2nd Ed., Morgan Kaufmann Publishers, Inc., (1994).				
<i>E.C.</i>		Hewlett-Packard Co., "PA-RISC 1.1 Architecture and Instruction Set", Manual Part No. 09740-90039, (1990).				
<i>E.C.</i>		MIPS Computer Systems, Inc., "MIPS R4000 User's Manual", Mtg. Part No. MB-00040, (1990).				
EXAMINER <i>Eun C. Lee</i>			DATE CONSIDERED <i>1/31/08</i>			

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SHEET 1 OF 11

INFORMATION DISCLOSURE CITATION IN AN APPLICATION (PTO-1449)				ATTY. DOCKET NO. 043876-0162	SERIAL NO. 10/757,851	
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U.S. PATENT DOCUMENTS						
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<i>E.C.</i>	US	4,858,349 A	05/14/1987	Gafken		
	US	4,852,098	07/25/1989	Brechard et al.		
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	US	5,638,351	06/03/1998	Lee		
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<i>E.C.</i>		JP 3268024	11/28/1991	Hitachi Ltd.		Yes
		EP 0 468 820 A2	01/29/1992	Fujitsu Limited		No
		WO 93/01565	01/21/1993	Seiko Epson Corporation		
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	<i>E.C.</i>		WO 97/07450	02/27/1997	Microunity Systems Engineering, Inc.	
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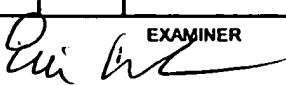
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<i>EC</i>	L-5	M. Awaga et al., "The µVP 64-bit Vector Coprocessor: A New Implementation of High-Performance Numerical Computation", IEEE Micro, Vol. 13, No. 5, October 1993, p.24-36.	
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<i>EC</i>	L-35	Toyokura, M., "A Video DSP with a Macroblock-Level-Pipeline and a SIMD Type Vector-Pipeline Architecture for MPEG2 CODEC," ISSCC94, Section 4, Video and Communications Signal Processors, Paper WP 4.5, 1994 pp. 74-75.	
<i>EC</i>	L-36	Ide, et al., "A 320-MFLOPS CMOS Floating-point Processing Unit for Superscalar Processors," Nobuhiro Ide, et. Al. IEEE Tokyo Section, Denshe Tokyo No. 32, 1993, p. 103-109.	
<i>EC</i>	L-37	Papadopoulos et al., "*T: Integrated Building Blocks for Parallel Computing," ACM (1993) p. 824- and p. 625-63.5	
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<i>EC</i>	L-40	K. Diefendorff, M. Allen, The Motorola 88110 Superscalar RISC Microprocessor, IEEE Micro, April 1992, p. 157-162.	
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<i>Eric V</i>		EXAMINER	DATE CONSIDERED <i>1/31/08</i>

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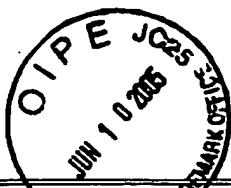
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